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	Subclass
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ISSUE CLASSIFICATION	

Best Available Copy**U.S. UTILITY Patent Application**SC 2
SC 8 29

O.I.P.E.		PATENT DATE
SCANNED	14/2	Q.A. U

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/675997	D	455	457	26843	Everb

Applicant
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Mountain View, CA
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TITLE

Patented Invention - Process for Producing a Uniformly Doped Silicon Wafer

PTO-2040
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Assistant Examiner)	(Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
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